

## Preface

On behalf of the Program Committee, we are delighted to present the Proceedings of the 1st Microarchitectural Security Conference ( $\mu$ ASC 2025). The  $\mu$ ASC conference fills a gap in the security research landscape, focusing on the advancement of our understanding of the security of microarchitectures of all kinds and on all layers. Our goal this year was to attract high-quality research from academia and industry through various formats accommodating the different needs of the researchers. For this reason, we accepted three types of submissions: papers, posters, and talks.

Although it was  $\mu$ ASC's first year, we received 22 valid submissions. We assigned 3 reviewers to each submission, resulting in 66 reviews overall. Paper reviews were conducted as double-blind reviews, for posters and talks, the authors were able to choose between single-blind and double-blind peer review. We received 7 paper submissions, out of which the program committee accepted 6 papers, resulting in an acceptance rate of 86%. We received 9 talk submissions, out of which the program committee accepted 8 talks, resulting in an acceptance rate of 89%. We accepted all 6 poster submissions.

We thank all members of the  $\mu$ ASC Steering Committee for their hard work on laying the foundation for this new conference. We would also like to express our deep gratitude to them for volunteering as the first Program Committee as well, for their reviewing effort, for discussions (more than 80 comments for 22 submissions), and for reviewing revisions of shepherded submissions. We appreciate the support from our sponsor Google as well as our host institutions.

Finally, we thank the authors who submitted their work and presented their contributions, as well as the attendees who participated in the conference.  $\mu$ ASC was held on campus at Ruhr University Bochum in Germany. With about 80 participants, the conference's first year was already much larger than we anticipated and it created a unique opportunity to network, discuss, engage, and socialize with other researchers and practitioners interested in all kinds of microarchitectures. We look forward to future years and future contributions.

February 2025

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